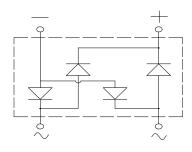


## Low VF Bridge Rectifiers





#### **Features**

- UL recognition, file #E313149
- Glass passivated chip junction
- Ideal for automated placement
- High surge current capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

#### Typical Applications

General purpose use in AC/DC bridge full wave rectification for SMPS, lighting ballaster, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

#### **Mechanical Data**

• Package: YBS

Molding compound meets UL 94 V-0 flammability rating, -compliant, Halogen-free

• **Terminals**: Tin plated leads, solderable per

J-STD-002 and JESD22-B102
• Polarity: As marked on body

### ■Maximum Ratings (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	YBSL3006A	
Device marking code			YBSL3006A	
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	٧	600	
Maximum RMS Voltage	V <sub>RMS</sub>	V	420	
Maximum DC blocking Voltage	V <sub>DC</sub>	V	600	
Average rectified output current @60Hz sine wave, R-load, Tc=125°C	lo	А	3.0	
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave,1 cycle, Tj=25°C			120	120
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25°C	- I <sub>FSM</sub>	А	240	
Current squared time @1ms≤t<8.3ms Tj=25°C,Rating of per diode	l <sup>2</sup> t	A <sup>2</sup> s	59.8	
Storage temperature	Tstg	°C	-55 ~ +150	
Junction temperature	Tj	°C	-55 ~ +150	

### **■Electrical Characteristics** (T<sub>a</sub>=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	Min	Тур	Max
Instantaneous forward voltage drop per diode	VF	V	IFM=1.5A	0.7	0.86	0.92
DC reverse current at rated DC blocking voltage per diode	lR	μА	T <sub>j</sub> =25°C	•	0.085	5
			Tj =125°C	-	25	100
Junction capacitance	Cj	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	25	45	65

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### ■Thermal Characteristics (Ta=25°C Unless otherwise specified)

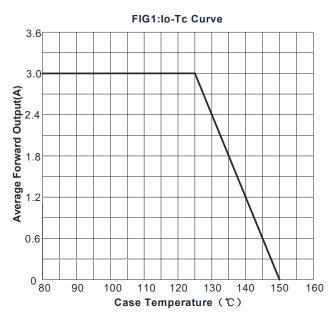
	PARAMETER	SYMBOL	UNIT	YBSL3006A
Tomical	Between Junction and Ambient	R <sub>θJ-A</sub>		55.0
Typical Thermal Resistance	Between Junction and Lead	$R_{\theta J-L}$	°C/W	15.0
i tesisiance	Between Junction and Case	R <sub>θJ-C</sub>		8.0

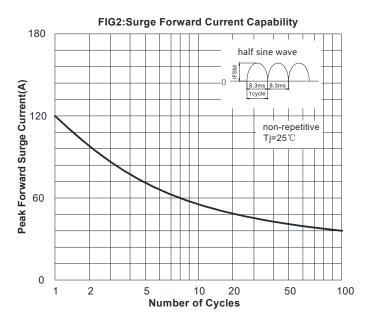
Note: Device mounted on P.C.B with 35mm\*25mm\*1.7mm.

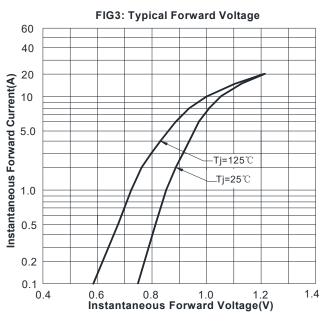
**■Ordering Information** (Example)

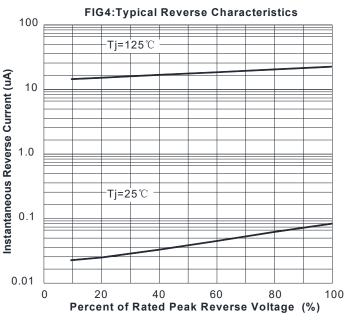
PREFERED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YBSL3006A	F1	Approximate 0.220	3000	1	42000	13" reel

### ■ Characteristics(Typical)



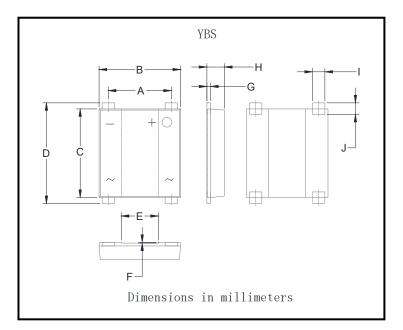






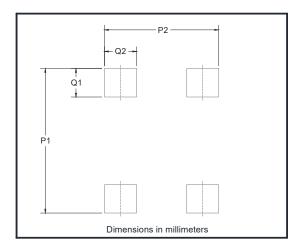


#### **■ Outline Dimensions**



YBS				
Dim	Min	Max		
Α	5.00	5.20		
В	6.50	6.70		
С	7.20	7.40		
D	7.90	8.60		
E	2.90	3.10		
F	0.04	0.08		
G	0.27	0.40		
Н	1.30	1.50		
1	0.95	1.15		
J	0.70	1.05		

## ■ Suggested pad layout



Dim	Min
P1	9.15
P2	7.10
Q1	1.80
Q2	2.00



#### **Disclaimer**

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