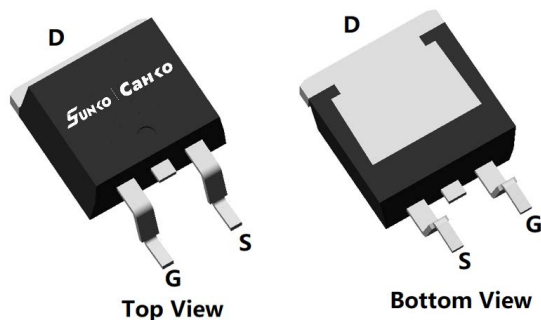
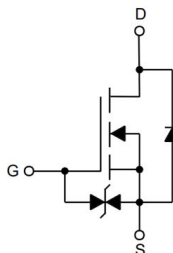


N-Channel Enhancement Mode Field Effect Transistor



TO-263



Product Summary

- V_{DS} 800V
- I_D 5.5A
- $R_{DS(ON)}$ (at $V_{GS}=10V$) $<1100m\Omega$
- 100% EAS Tested
- 100% ∇V_{DS} Tested

General Description

- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power switching application
- Uninterruptible power supply
- DC-DC convertor

Limiting values

Parameter	Conditions		Symbol	Min	Max	Unit
Drain-source Voltage			V _{DS}	-	800	V
Gate-source Voltage			V _{GS}	-30	30	
Continuous Drain Current (Note 1,2)	Steady-State	T _A =25°C,V _{GS} =10V	I _D	-	1.0	A
		T _A =100°C,V _{GS} =10V		-	0.6	
Continuous Drain Current (Note 1,3)	Steady-State	T _C =25°C,V _{GS} =10V,Chip limitation		-	5.5	
		T _C =100°C,V _{GS} =10V		-	3.4	
Pulsed Drain Current	T _C =25°C,t _p ≤10μs		I _{DM}	-	13	
Maximum Body-Diode Continuous Current	T _C =25°C		I _S		5.5	
Maximum Body-Diode Pulsed Current	T _C =25°C,t _p ≤10μs		I _{SM}	-	13	
Avalanche Energy (non-repetitive)	T _J =25°C,V _G =10V,R _G =25Ω,L=30mH,I _{AS} =2.7A		EAS	-	109.35	mJ
Total Power Dissipation (Note 1,2)	Steady-State	T _A =25°C	P _D	-	3	W
		T _A =100°C		-	1.2	
Total Power Dissipation (Note 1,3)	Steady-State	T _C =25°C		-	89	
		T _C =100°C		-	35	
MOSFET dv/dt Ruggedness	V _{DS} =0...400V,R _g =0Ω		dv/dt	-	8	V/ns
Reverse Diode dv/dt	V _{DS} =0...400V,I _D <=2A,di/dt=200A/us		dv/dt	-	7.9	
Maximum Diode Commutation Speed	V _{DS} =0...400V,I _D <=2A,R _g =0Ω		dif/dt	-	629	A/us
Junction and Storage Temperature Range			T _J ,T _{STG}	-55	150	°C

Thermal Resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	Steady-State	$R_{\theta JA}$	-	42	$^\circ C/W$
Thermal Resistance Junction-to-Case	Steady-State	$R_{\theta JC}$	-	1.4	

Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
SCB1K1C80BZJ	F2	SCB1K1C80BZ	800	/	8000	13" reel

■ Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A, T_J=25^{\circ}C$	800	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=800V, V_{GS}=0V, T_J=25^{\circ}C$	-	-	1	μA
		$V_{DS}=800V, V_{GS}=0V, T_J=125^{\circ}C$	-	-	100	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V, T_J=25^{\circ}C$	-	-	± 10	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A, T_J=25^{\circ}C$	2.5	3.6	4.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3A, T_J=25^{\circ}C$	-	775	1100	m Ω
		$V_{GS}=10V, I_D=3A, T_J=150^{\circ}C$	-	2078	2949	m Ω
Diode Forward Voltage	V_{SD}	$I_S=5.5A, V_{GS}=0V, T_J=25^{\circ}C$	-	0.81	1.2	V
Gate Resistance	R_G	$f=1MHz, T_J=25^{\circ}C$	-	34	-	Ω
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=400V, V_{GS}=0V, f=1MHz, T_J=25^{\circ}C$	-	583	-	pF
Output Capacitance	C_{oss}		-	15	-	
Reverse Transfer Capacitance	C_{rss}		-	1.2	-	
Effective output capacitance, energy related	$C_{o(er)}$	$V_{DS}=0 \dots 400V, V_{GS}=0V, f=1MHz, T_J=25^{\circ}C$	-	19	-	
Effective output capacitance, time related	$C_{o(tr)}$		-	70	-	
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS}=10V, V_{DS}=400V, I_D=3A, T_J=25^{\circ}C$	-	14.6	-	nC
Gate-Source Charge	Q_{gs}		-	3.1	-	
Gate-Drain Charge	Q_{gd}		-	6.6	-	
Reverse Recovery Charge	Q_{rr}	$I_F=3A, di/dt=100A/\mu s, V_{GS}=0V, V_R=400V, T_J=25^{\circ}C$	-	1441	-	nC
Reverse Recovery Time	t_{rr}		-	183	-	ns
Peak Reverse Recovery Current	I_{rrm}		-	12.6	-	A
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=10V, V_{DS}=400V, I_D=3A, R_{GEN}=3\Omega, T_J=25^{\circ}C$	-	68	-	ns
Turn-on Rise Time	t_r		-	35	-	
Turn-off Delay Time	$t_{D(off)}$		-	55	-	
Turn-off Fall Time	t_f		-	58	-	

Note:

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. The value of $R_{\theta JA}$ is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with $T_A=25^\circ C$. The maximum allowed junction temperature of $150^\circ C$. The value in any given application depends on the user's specific board design.
3. Thermal resistance from junction to soldering point (on the exposed drain pad).

■ Typical Electrical and Thermal Characteristics Diagrams

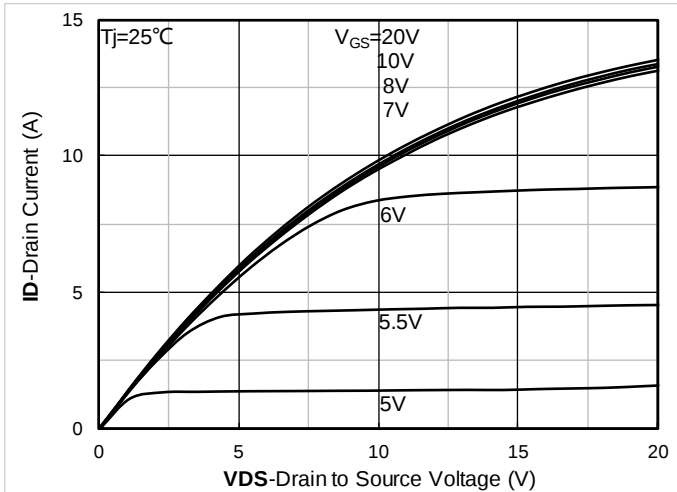


Figure 1. Output Characteristics; typical values

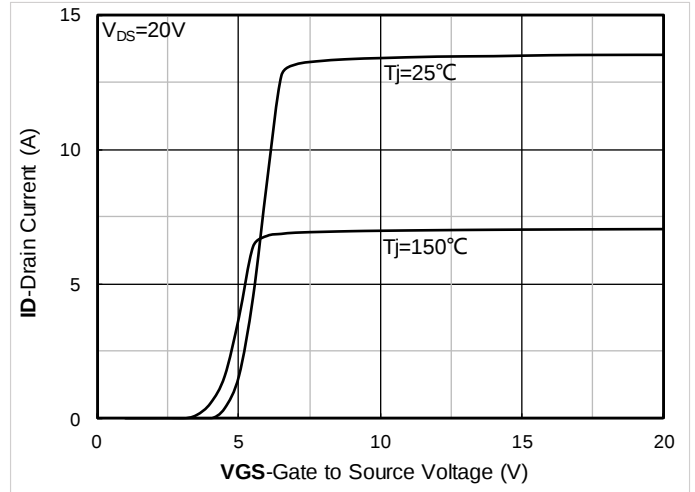


Figure 2. Transfer Characteristics; typical values

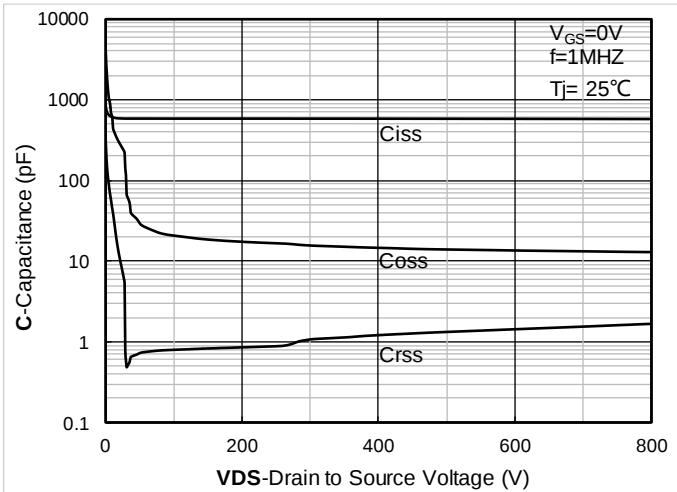


Figure 3. Capacitance Characteristics; typical values

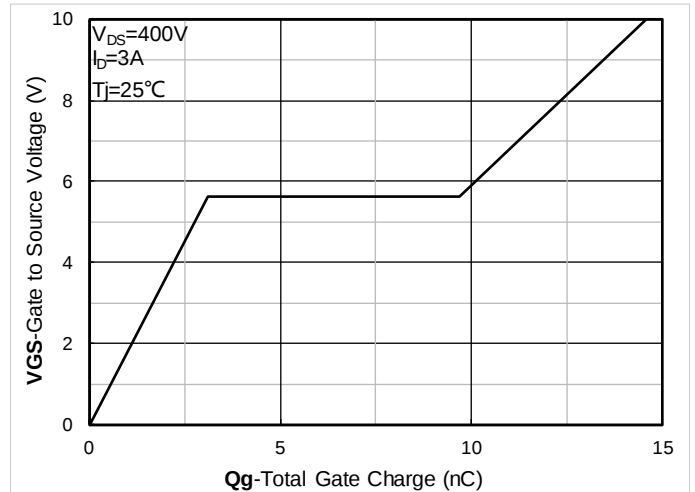


Figure 4. Gate Charge; typical values

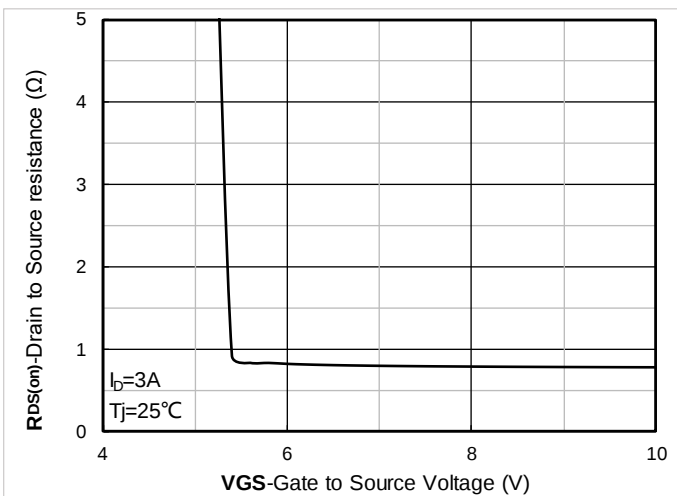


Figure 5. On-Resistance vs. Gate to Source Voltage; typical values

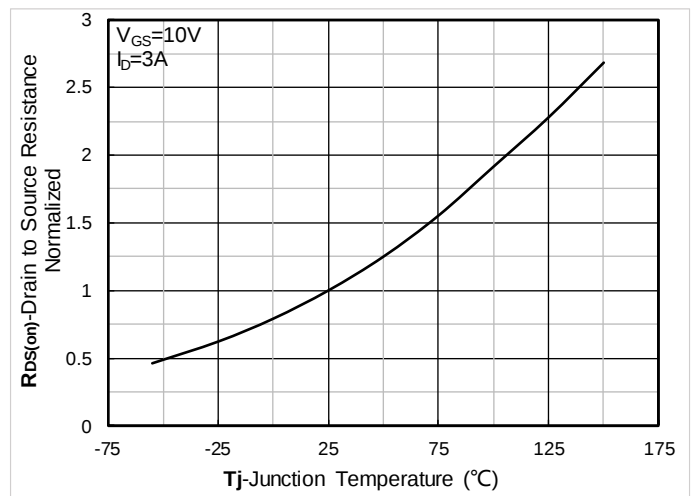


Figure 6. Normalized On-Resistance

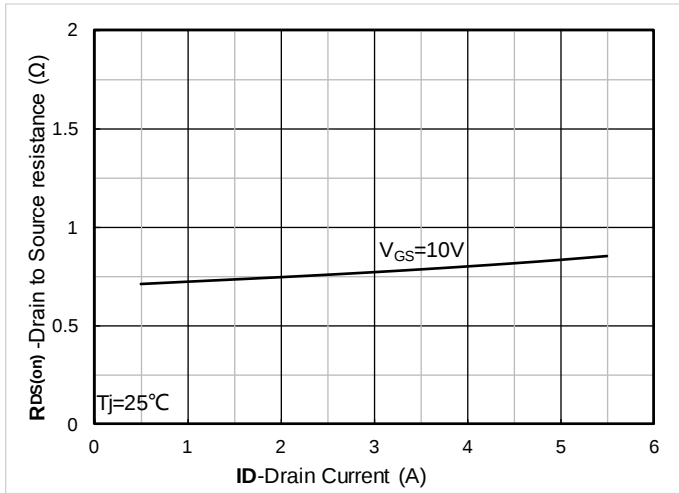


Figure 7. RDS(on) vs. Drain Current; typical values

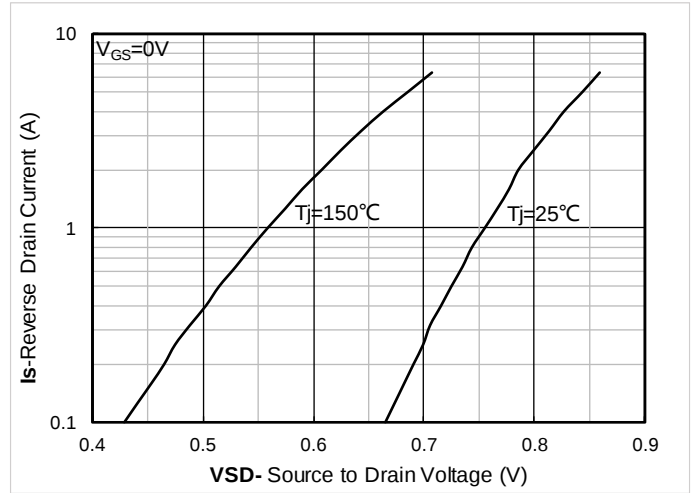


Figure 8. Forward characteristics of reverse diode; typical values

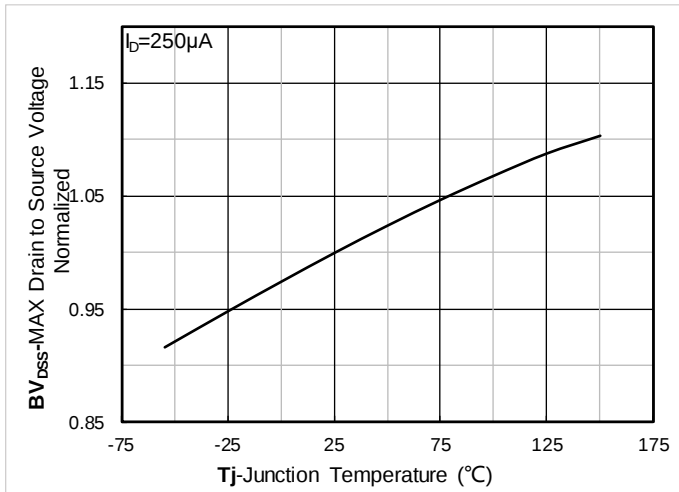


Figure 9. Normalized breakdown voltage

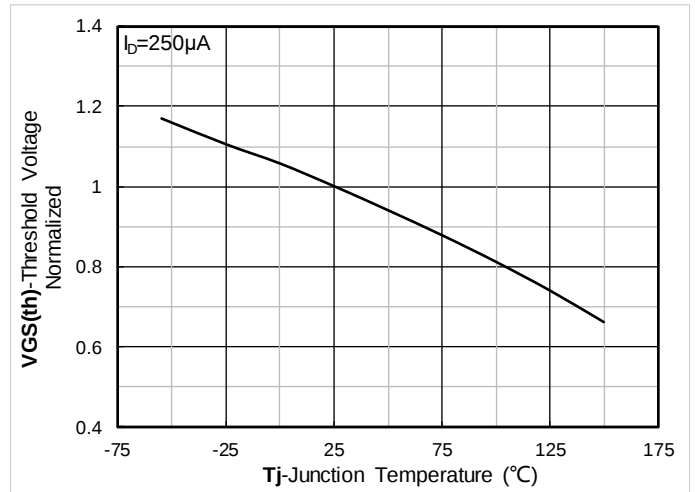


Figure 10. Normalized Threshold voltage

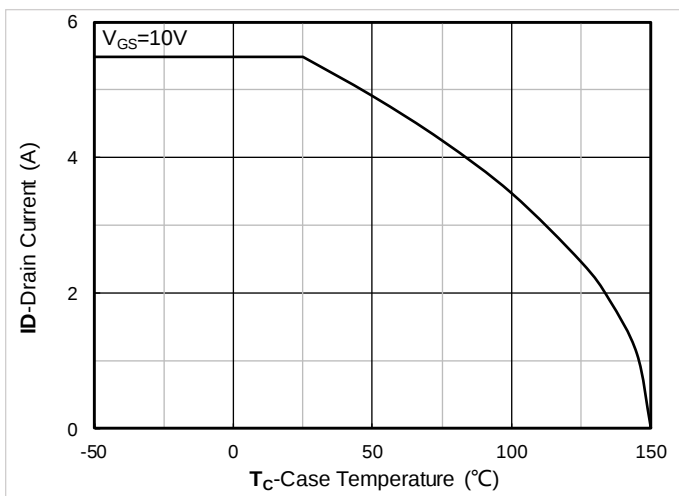


Figure 11. Current dissipation

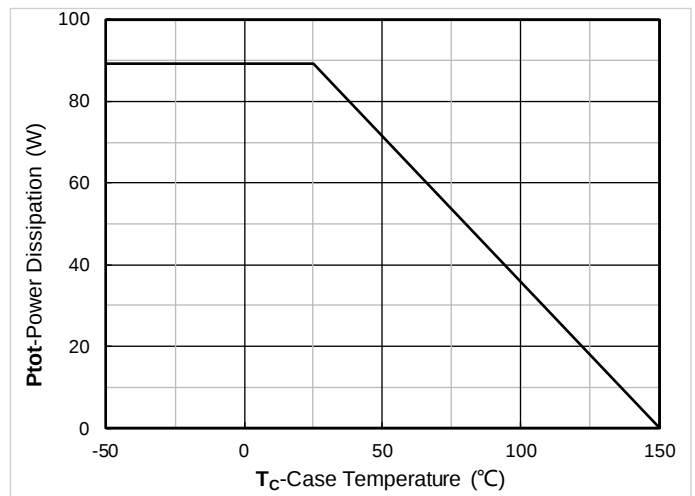


Figure 12. Power dissipation

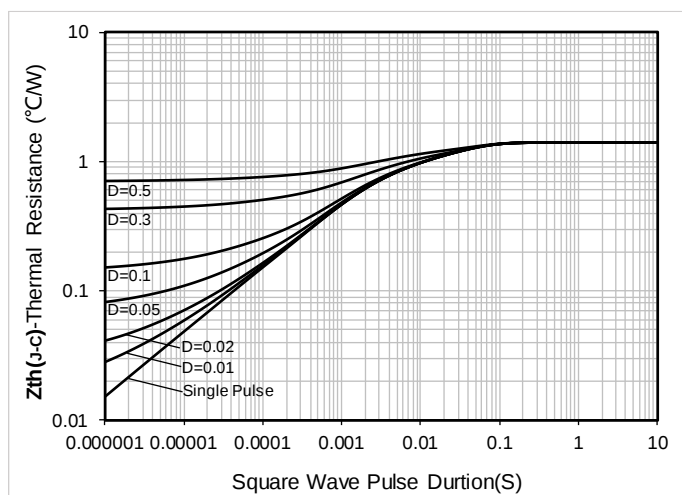


Figure 13. Maximum Transient Thermal Impedance

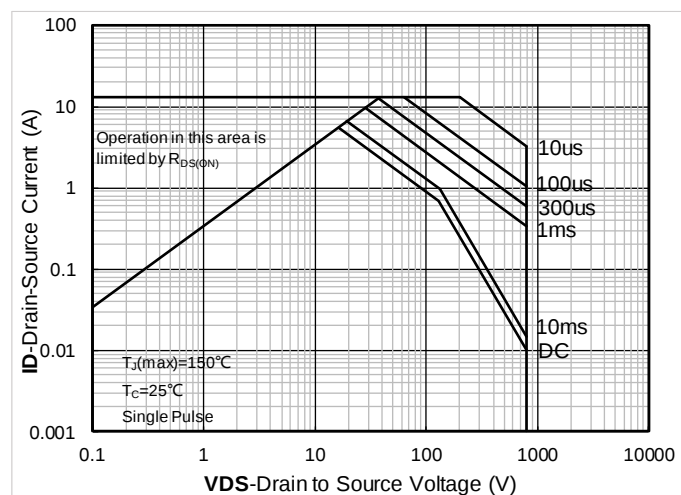
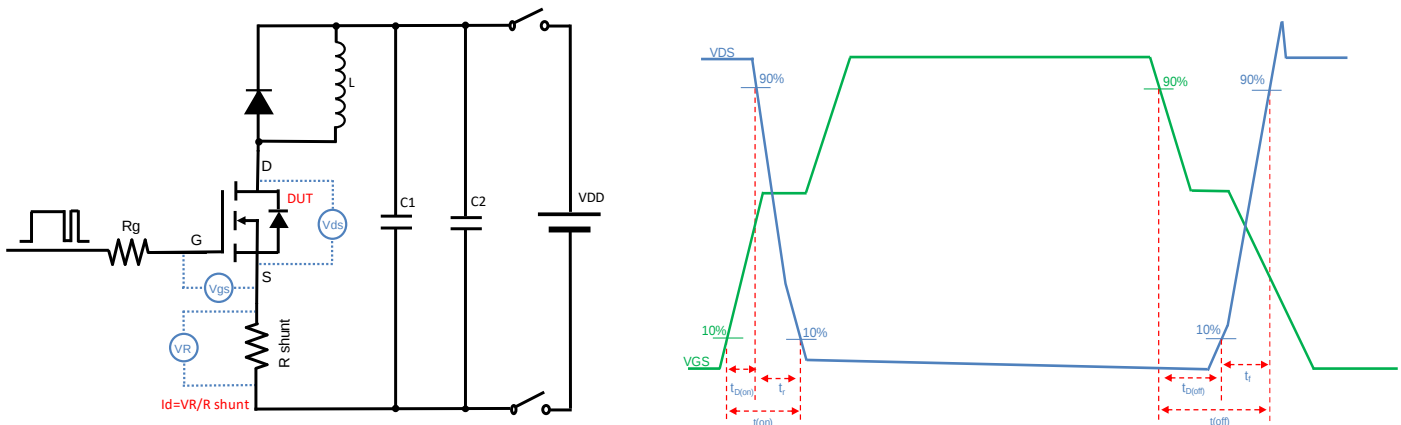
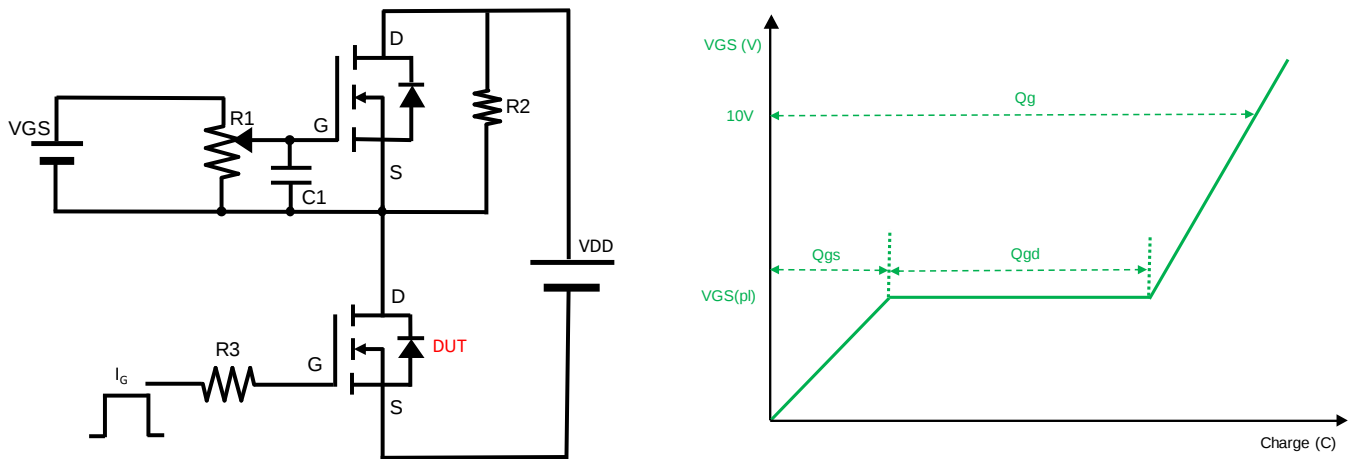
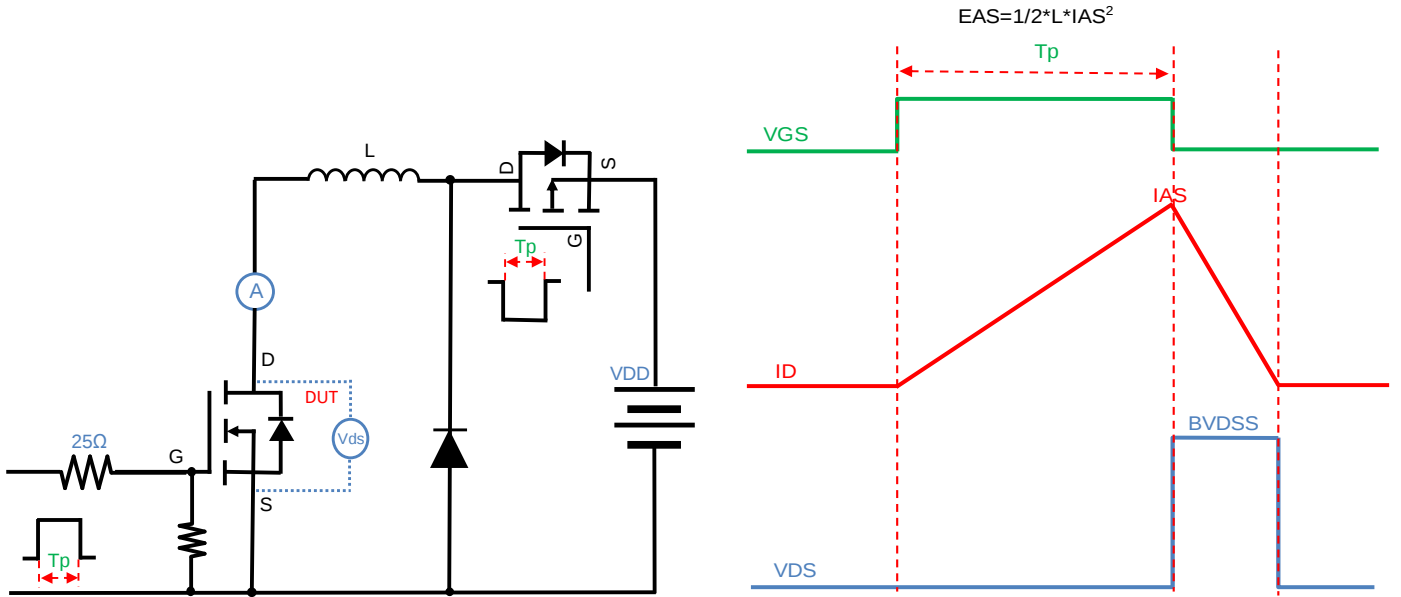


Figure 14. Safe Operation Area

■ Test Circuits & Waveforms



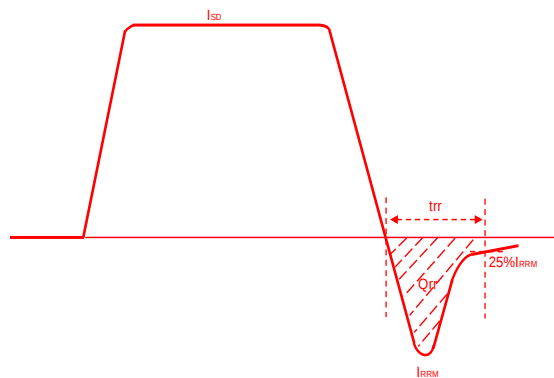
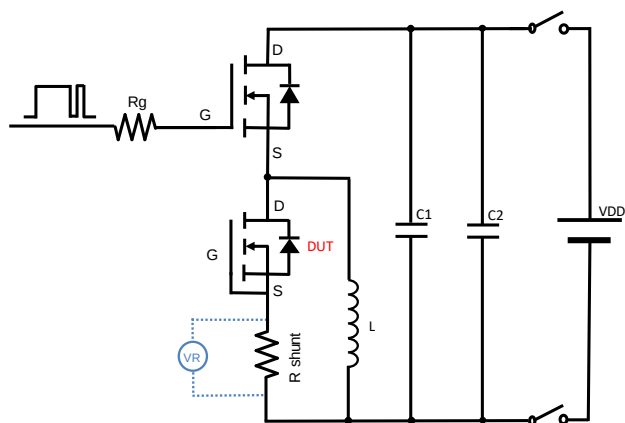
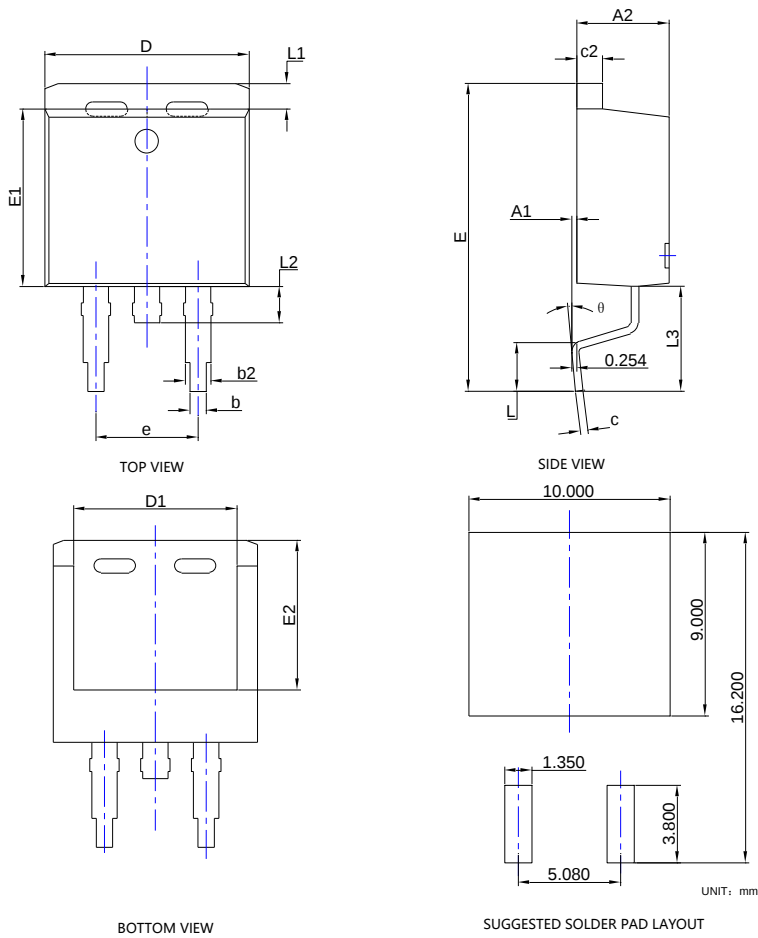


Figure D. Diode Recovery Test Circuit & Waveform

■ TO-263-B Package information



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETER	
	MIN.	MAX.	MIN.	MAX.
A1	0.000	0.010	0.000	0.254
A2	0.160	0.190	4.064	4.826
b	0.020	0.039	0.508	0.991
b2	0.045	0.070	1.143	1.778
c	0.015	0.029	0.381	0.737
c2	0.045	0.065	1.143	1.651
D	0.380	0.420	9.652	10.668
D1	0.245	---	6.223	---
E	0.575	0.625	14.605	15.875
E1	0.330	0.380	8.382	9.652
E2	0.270	---	6.858	---
e	0.200BSC		5.08BSC	
L	0.070	0.110	1.778	2.794
L1	---	0.066	---	1.676
L2	---	0.070	---	1.778
L3	0.188	0.208	4.780	5.280
θ	0°	8°	0°	8°

NOTE:

- 1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
- 2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
- 3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

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