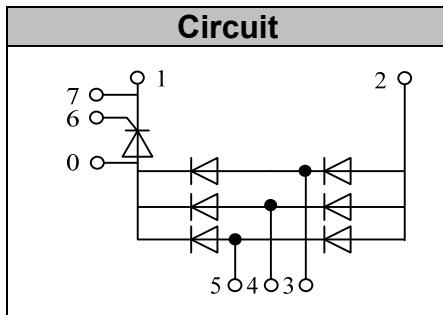




Three Phase Bridge + Thyristor

V_{RRM} / V_{DRM} 800 to 1800V
I_{FAV} / I_{TAV} 75A



Features

- Blocking voltage: 800 to 1800V
- Three Phase Bridge and a Thyristor
- Isolated Module package

Applications

- Inverter for AC or DC motor control
- Current stabilized power supply
- Switching power supply
- UL recognized applied for file no. E360040

Module Type

TYPE	V _{RRM} / V _{DRM}	V _{RSM}
MT75DT08L1	800V	900V
MT75DT12L1	1200V	1300V
MT75DT16L1	1600V	1700V
MT75DT18L1	1800V	1900V

◆ Diode

Maximum Ratings

Symbol	Item	Conditions	Values	Units
I _D	Output Current(D.C.)	T _c =101°C Three phase full wave	75	A
I _{FSM}	Surge forward current	t=10mS T _{vj} =45°C	920	A
i ² t	Circuit Fusing Consideration		4200	A ² s
V _{Isol}	Isolation Breakdown Voltage(R.M.S)	a.c.50HZ;r.m.s.;1min	3000	V
T _{vj}	Operating Junction Temperature		-40 to +150	°C
T _{stg}	Storage Temperature		-40 to +125	°C
M _t	Mounting Torque	To terminals(M5)	3±15%	Nm
M _s		To heatsink(M5)	3±15%	Nm
Weight		Module (Approximately)	210	g

Thermal Characteristics

Symbol	Item	Conditions	Values	Units
R _{th(j-c)}	Thermal Impedance, max.	Junction to Case(TOTAL)	0.20	°C/W
R _{th(c-s)}	Thermal Impedance, max.	Case to Heatsink	0.10	°C/W

Electrical Characteristics

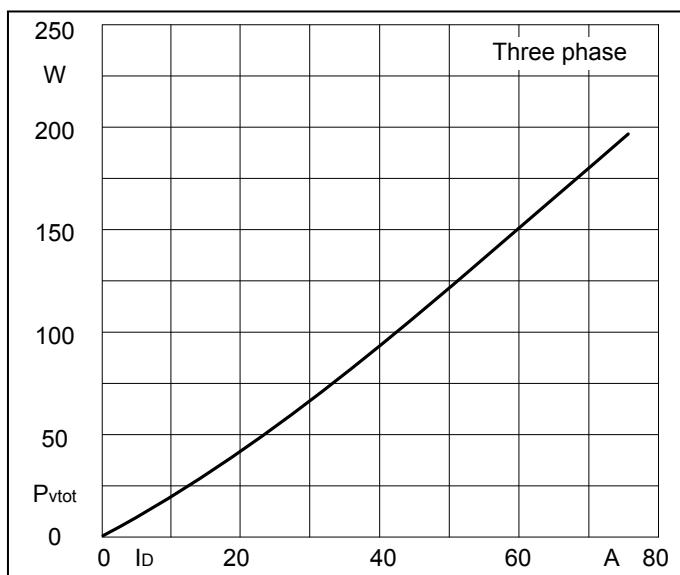
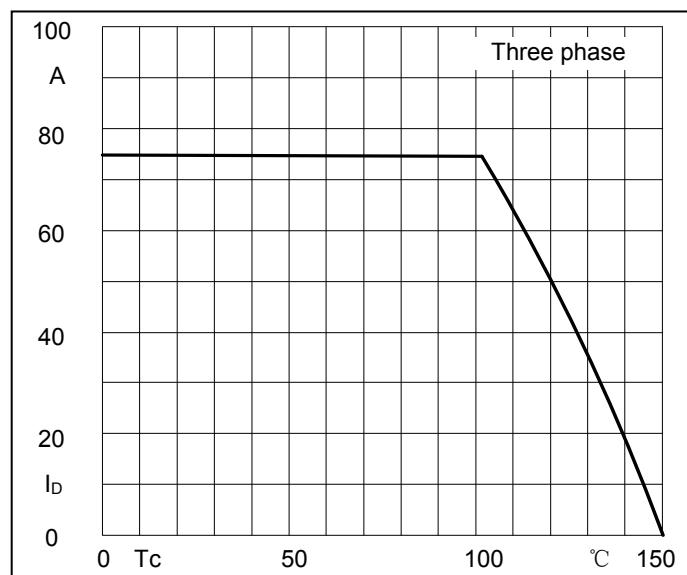
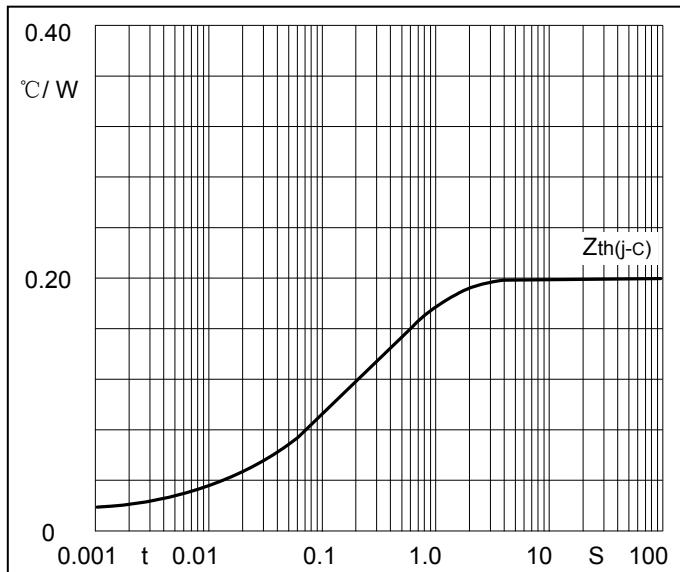
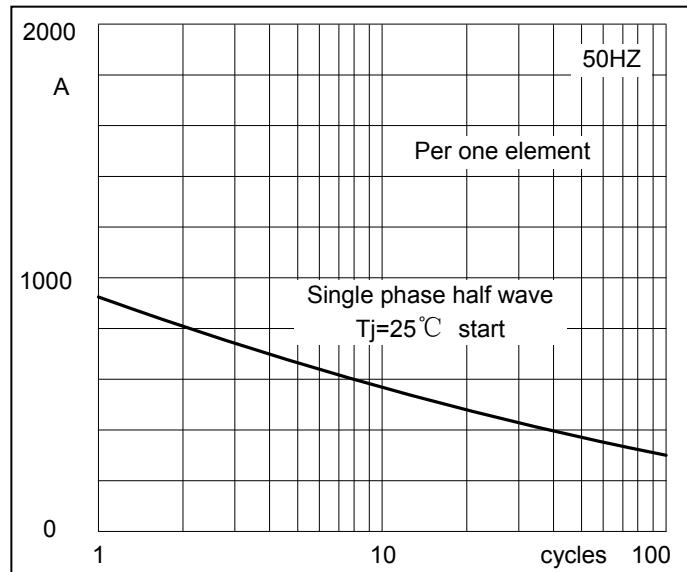
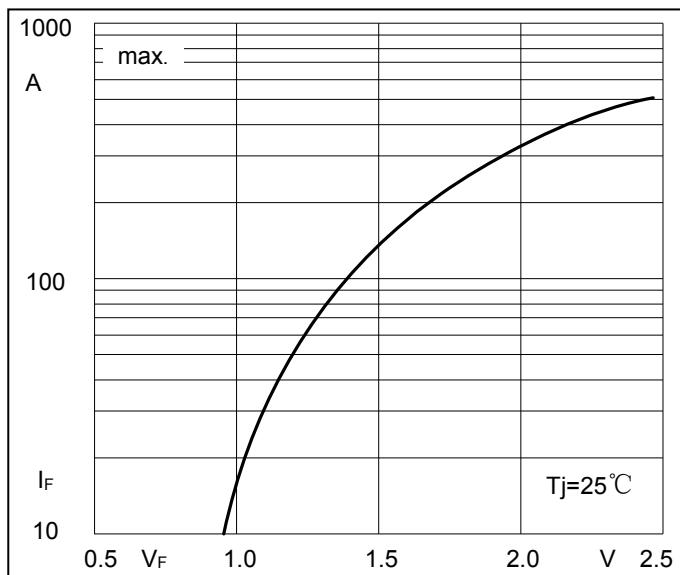
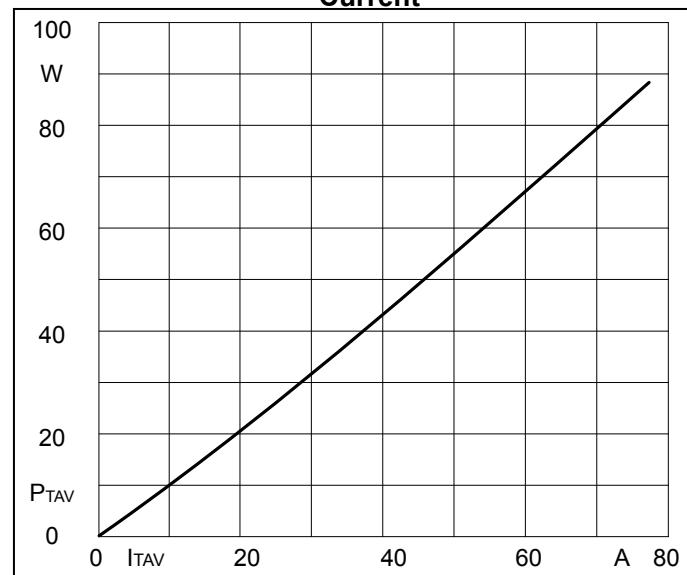
Symbol	Item	Conditions	Values	Units
V _{FM}	Forward Voltage Drop, max.	T=25°C I _F =100A	1.40	V
I _{IRRM}	Repetitive Peak Reverse Current, max.	T _{vj} =25°C V _{RD} =V _{RRM} T _{vj} =150°C V _{RD} =V _{RRM}	≤0.5 ≤6	mA mA

◆ Thyristor
Maximum Ratings

Symbol	Item	Conditions	Values	Units
I _{TAV}	Average On-State Current	T _c =99°C, Single Phase half wave 180° conduction	75	A
I _{TSM}	Surge On-State Current	T _{VJ} =45°C t=10ms (50Hz), sine VR=0	920	A
i ² t	Circuit Fusing Consideration		4200	A ² s
Visol	Isolation Breakdown Voltage(R.M.S)	a.c.50HZ;r.m.s.;1 min	3000	V
T _{vj}	Operating Junction Temperature		-40 to +125	°C
T _{stg}	Storage Temperature		-40 to +125	°C
M _t	Mounting Torque	To terminals(M5)	3±15%	Nm
M _s		To heatsink(M5)	3±15%	Nm
di/dt	Critical Rate of Rise of On-State Current	T _{VJ} =T _{VJM} , V _D =1/2V _{DRM} , I _G =100mA di _G /dt=0.1A/μs	150	A/μs
dv/dt	Critical Rate of Rise of Off-State Voltage, min.	T _J =T _{VJM} , V _D =2/3V _{DRM} , linear voltage rise	500	V/μs

Electrical and Thermal Characteristics

Symbol	Item	Conditions	Values			Units
			Min.	Typ.	Max.	
V _{TM}	Peak On-State Voltage, max.	T=25°C I _T =100A			1.30	V
I _{RRM} /I _{DRM}	Repetitive Peak Reverse Current, max. / Repetitive Peak Off-State Current, max.	T _{VJ} =T _{VJM} , V _R =V _{RRM} , V _D =V _{DRM}			20	mA
V _{GT}	Gate Trigger Voltage, max.	T _{VJ} =25°C, V _D =6V			3	V
I _{GT}	Gate Trigger Current, max.	T _{VJ} =25°C, V _D =6V			150	mA
R _{th(j-c)}	Thermal Impedance, max.	Junction to Case			0.30	°C/W
R _{th(c-s)}	Thermal Impedance, max.	Case to Heatsink			0.10	°C/W

Performance Curves**Fig1. Power dissipation****Fig2. Forward Current Derating Curve****Fig3. Transient thermal impedance****Fig4. Max Non-Repetitive Forward Surge Current****Fig5. Forward Characteristics****Fig6. SCR Power dissipation**

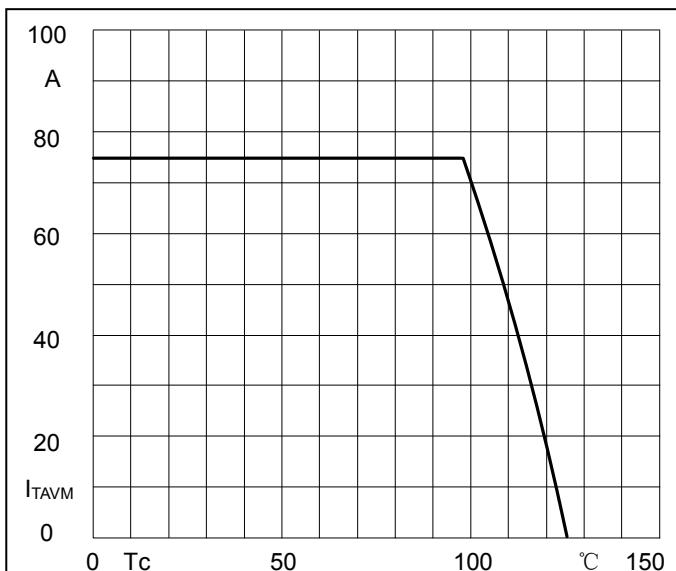


Fig7. SCR Forward Current Derating Curve

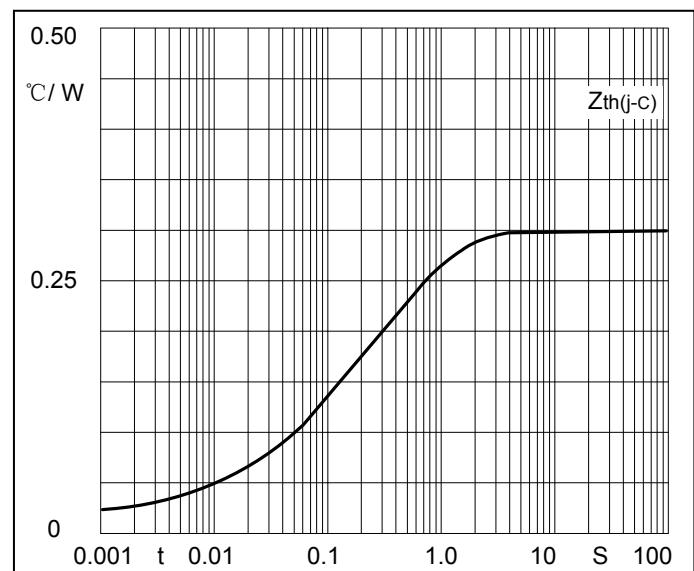


Fig8. SCR Transient thermal impedance

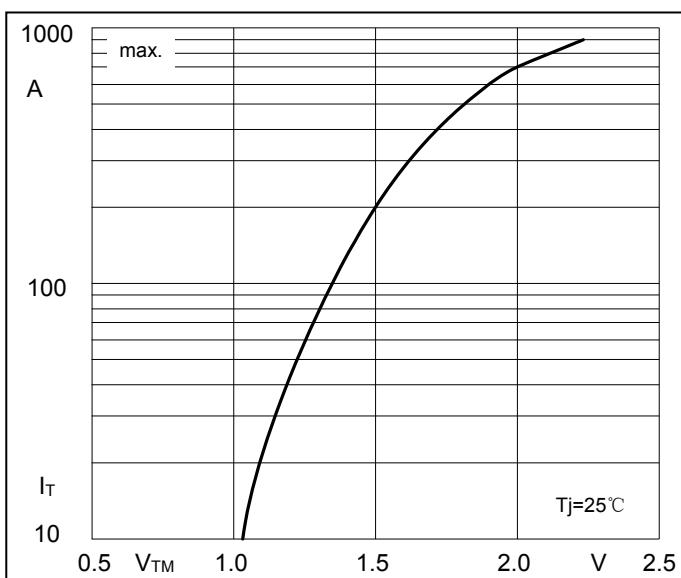


Fig9. SCR Forward Characteristics

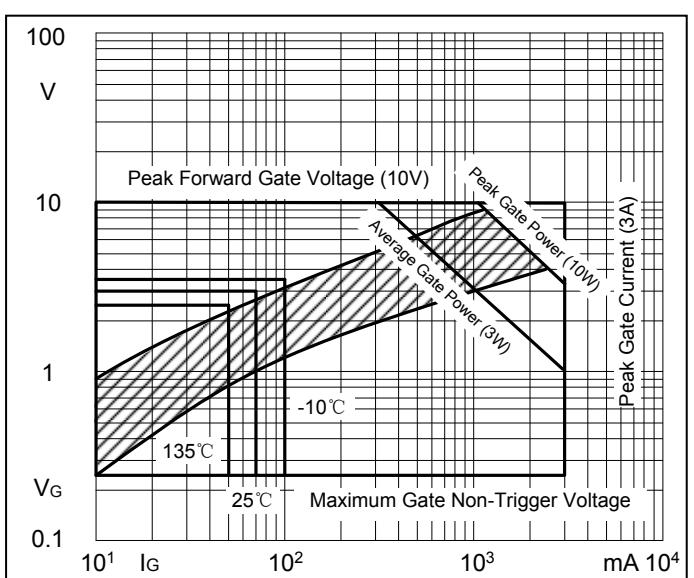
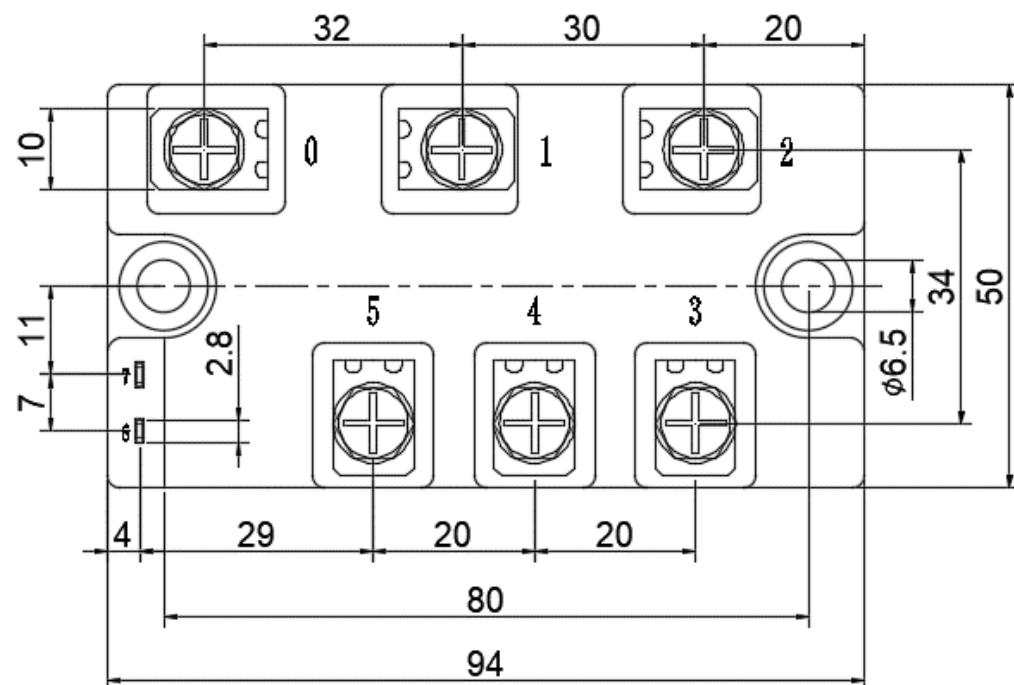
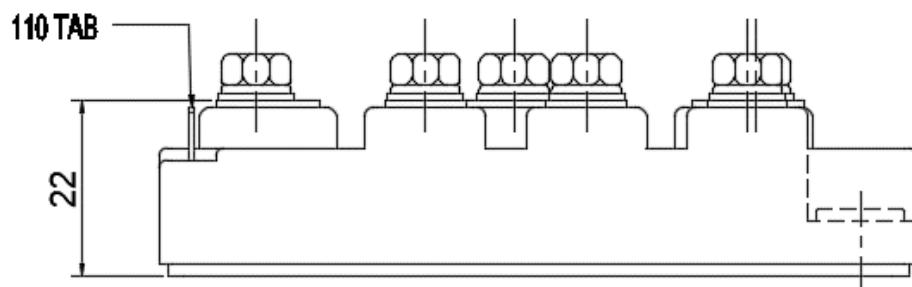


Fig10. Gate trigger Characteristics

Package Outline Information

CASE: L1



Dimensions in mm